

FET 58 is turned on, allowing output FET 53 to be connected to a load 18 and drive the CDS circuitry 55 directly.

Please replace the paragraph beginning at page 5, line 30 with the following paragraph:

The drain 30 of FET 24 is the output of the differential pair and is connected to CDS 34.

Please replace the paragraph beginning at page 10, line 15 with the following paragraph:

The purpose of selecting the current column and pre-selecting the next three columns is to allow the video bus to charge up to the proper value and settle prior to being demultiplexed by circuit 24 as shown in Figure 7. By pre-selecting the three columns (or pixel time constants) ahead of time, the column video processing circuitry only has to drive the video bus at one quarter the actual pixel read rate (one-fourth the bandwidth) and therefore can be made smaller and lower power than they would otherwise have to be. Also, since each column is connected to only one out of every four columns the video bus has only one-fourth of the capacitance, because there are only one fourth of the transmission gates (or switches) to drive. As a result, each column selected also pre-selects the next three columns in sequence. The column selection sequence remains conventional, with out the need for post processing reconstruction of the original image required of multi-port imagers. The analog pre-charging is done at a one-fourth the bandwidth in Figure 7 than the prior art conventional single ended video bus and only the demultiplexing is done at the normal bandwidth.

REMARKS

Applicants have amended the specification as set forth herein to correct some typographical errors. No new matter has been added by these amendments. Applicants note with appreciation the Office's indication that claims 32-48 would be allowable if rewritten or amended to overcome the rejection under 35 U.S.C. Section 112. In view of the following amendments and remarks, Applicants hereby request further examination and reconsideration of the application, and allowance of claims 32-48.